

IN THE CLAIMS

Please cancel claim 6 without prejudice, and amend claims 1, 12, 13, 15, 17, 26, 27, 41, 43 and 47 as follows:

1. (currently amended) A liquid crystal display, comprising:

a first substrate including a pixel electrode having a first portion and a second portion formed thereon;

a second substrate including a common electrode formed thereon, wherein the second substrate is spaced apart from the first substrate by a gap; [[and]]

at least one cutout formed in the common electrode, wherein the at least one cutout is aligned with a space between the first and second portions; and

a plurality of data lines for transmitting data voltages formed on the first substrate, wherein the data lines are interposed between the first and second portions.

2. (previously presented) The liquid crystal display as recited in claim 1, wherein the first portion and the second portion are connected to each other.

3. (previously presented) The liquid crystal display as recited in claim 1, wherein the gap includes a liquid crystal layer configured for housing liquid crystal molecules, and the at least one cutout includes a first edge aligned parallel to an edge of the first portion and a second edge aligned parallel to an edge of the second portion.

4. (previously presented) The liquid crystal display as recited in claim 3, wherein a component of an electric field generated between the common and the pixel electrode for causing a change in tilt direction of the liquid crystal molecules aligns at least one of perpendicular to the first edge of the cutout, perpendicular to the second edge of the cutout, perpendicular to the edge of the first portion and perpendicular to the edge of the second portion.

5. (previously presented) The liquid crystal display as recited in claim 1, wherein the at least one cutout has a width within the range of about 9 to about 12 microns.

6. (canceled).

7. (previously presented) The liquid crystal display as recited in claim 3, wherein an electric field is generated between the first and second portions, and a direction of the electric field is at least one of perpendicular to the first edge of the at least one cutout and perpendicular to the second edge of the at least one cutout.

8. (previously presented) The liquid crystal display as recited in claim 1, wherein a voltage having an opposite polarity with respect to a voltage applied to the common electrode is applied to one of the first portion and the second portion and an electric field is generated between the first portion and the second portion.

9. (previously presented) The liquid crystal display of claim 8, wherein the electric

field is due to a voltage difference between the first portion and the second portion.

10. (original) The liquid crystal display as recited in claim 1, further comprising:

at least one gate electrode formed on the first substrate; and

at least two transistors formed on the first substrate and symmetrically disposed about the at least one gate electrode for creating a non-varying parasitic capacitance between the at least one gate electrode and at least two drain electrodes of the at least two transistors across a plurality of shots of the first substrate.

11. (original) The liquid crystal display as recited in claim 10, wherein a pair of the symmetrically disposed transistors includes the at least one gate electrode, at least one source electrode, the at least two drain electrodes and at least one semiconductor island.

12. (currently amended) The liquid crystal display as recited in claim 1, ~~further comprising a plurality of data lines for transmitting data voltages formed on the first substrate,~~ wherein the first and second portions are symmetrically disposed about at least one data line of the plurality of data lines for creating a non-varying parasitic capacitance between the first and second portions and the at least one data line across a plurality of shots of the first substrate.

13. (currently amended) The liquid crystal display as recited in claim 1, further

comprising:

a plurality of gate lines for transmitting gate signals formed on the first substrate; and

a plurality of storage electrode lines for transmitting at least one predetermined voltage formed on the first substrate; and

~~a plurality of data lines for transmitting data voltages formed on the first~~
substrate.

14. (previously presented) The liquid crystal display as recited in claim 13, wherein at least one of the first portion and the second portion is positioned in an area enclosed by the plurality of gate lines, the plurality of storage electrode lines and the plurality of data lines.

15. (currently amended) The liquid crystal display as recited in claim ~~[[13]]~~1, wherein at least one of the first portion and the second portion overlap at least one data line of the plurality of data lines.

16. (original) The liquid crystal display as recited in claim 13, wherein the plurality of data lines intersect the plurality of gate lines and the plurality of storage lines.

17. (currently amended) The liquid crystal display as recited in claim ~~[[13]]~~1, wherein each data line of the plurality of data lines is curved and includes a plurality of pairs of oblique portions connected to each other to form a chevron.

18. (original) The liquid crystal display as recited in claim 17, wherein opposite ends of the oblique portions are connected to respective longitudinal portions that cross over gate electrodes.

19. (original) The liquid crystal display as recited in claim 18, wherein a length of each pair of the oblique portions is about one to about nine times a length of a longitudinal portion.

20. (original) The liquid crystal display as recited in claim 13, wherein at least one of the plurality of gate lines, the plurality of storage electrode lines and the plurality of data lines includes tapered sides, wherein an incline angle of the tapered sides with respect to a horizontal surface of the first substrate is within the range of about 30 to about 80 degrees.

21. (original) The liquid crystal display as recited in claim 13, wherein at least one of the plurality of gate lines, the plurality of storage electrode lines and the plurality of data lines includes a lower film and an upper film.

22. (original) The liquid crystal display as recited in claim 21, wherein the upper film includes one of aluminum and an aluminum alloy and the lower film includes one of chromium, molybdenum and a molybdenum alloy.

23. (original) The liquid crystal display as recited in claim 1, further comprising:

a plurality of storage electrodes formed on the first substrate; and

a plurality of drain electrodes formed on the first substrate, wherein at least one pair of drain electrodes of the plurality of drain electrodes overlaps at least one pair of storage electrodes of the plurality of storage electrodes.

24. (previously presented) The liquid crystal display as recited in claim 1, further comprising:

a plurality of drain electrodes formed on the first substrate, wherein the first portion and the second portion are respectively connected to a first drain electrode and a second drain electrode of the plurality of drain electrodes, and the first portion and the second portion receive data voltages from the first drain electrode and the second drain electrode, respectively.

25. (original) The liquid crystal display as recited in claim 1, further comprising a plurality of color filters formed on one of the first substrate and the second substrate, wherein two adjacent color filters of the plurality of color filters overlap each other.

26. (currently amended) The liquid crystal display as recited in claim 1, further comprising:

a gate insulating layer formed on the first substrate;

a plurality of semiconductor islands formed on the gate insulating layer;

a plurality of ohmic contacts formed on the semiconductor islands;

~~a plurality of data lines for transmitting data voltages formed on at least one of the ohmic contacts and the gate insulating layer; and~~

a plurality of drain electrodes formed on the ohmic contacts, wherein the semiconductor islands have essentially the same planar shapes as at least one of the data lines, the drain electrodes and the ohmic contacts, and wherein the plurality of data lines are formed on at least one of the ohmic contacts and the gate insulating layer.

27. (currently amended) The liquid crystal display as recited in claim 1, further comprising:

a gate insulating layer formed on the first substrate;

a plurality of semiconductor islands formed on the gate insulating layer;

a plurality of ohmic contacts formed on the semiconductor islands;

~~a plurality of data lines for transmitting data voltages formed on at least one of the ohmic contacts and the gate insulating layer; and~~

a plurality of drain electrodes formed on the ohmic contacts, wherein the plurality of data lines are formed on at least one of the ohmic contacts and the gate insulating layer, and wherein the data lines, the drain electrodes, the semiconductor islands and the ohmic contacts are simultaneously formed using one photolithography process.

28. - 40. (canceled)

41. (currently amended) A liquid crystal display, comprising:

a first substrate including a first electrode and a second electrode formed thereon;

a second substrate including a third electrode formed thereon, wherein the second substrate is spaced apart from the first substrate by a gap; [[and]]

at least one cutout formed in the third electrode, wherein the at least one cutout includes a first edge aligned parallel to an edge of the first electrode and a second edge aligned parallel to an edge of the second electrode;

a plurality of storage electrodes formed on the first substrate; and

a plurality of drain electrodes formed on the first substrate, wherein at least one pair of drain electrodes of the plurality of drain electrodes overlaps at least one pair of storage electrodes of the plurality of storage electrodes.

42. (canceled)

43. (currently amended) The liquid crystal display as recited in claim [[6]]1, wherein each data line of the plurality of data lines is curved and includes a plurality of pairs of oblique portions connected to each other to form a chevron.

44. (previously presented) The liquid crystal display as recited in claim 43, wherein the pixel electrode has at least two boundaries parallel to a boundary of the oblique portions.

45. (previously presented) The liquid crystal display as recited in claim 44, further comprising;

a plurality of gate lines intersecting the data line and transmitting gate signals;

and

a plurality of thin film transistors respectively connected to the gate lines, the data lines and the pixel electrode.

46. (previously presented) The liquid crystal display as recited in claim 45, wherein the thin film transistors have a first and a second thin film transistor respectively connected to the first and the second portions.

47. (currently amended) The liquid crystal display as recited in claim ~~[[6]]~~1, wherein at least edge portions of the first and the second portions overlap the data lines.